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JLY 25. 2001 **PTAS**

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UNITED STATES PATENT AND TRADEMARK OFFICE NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT CFFICE OF PETITIONS

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 703-308-9723. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, ASSIGNMENT DIVISION, BOX ASSIGNMENTS, CG-4, 1213 JEFFERSON DAVIS HWY, SUITE 320, WASHINGTON, D.C. 20231.

RECORDATION DATE: 05/03/2001

REEL/FRAME: 011776/0678

NUMBER OF PAGES: 3

BRIEF: ASSIGNMENT OF ASSIGNOR''S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

HENRY, PAUL M.

DOC DATE: 05/02/2001

ASSIGNOR:

SMITH, GREGORY J.

DOC DATE: 05/02/2001

ASSIGNEE:

NATIONAL SEMICONDUCTOR CORPORATION 2900 SEMICONDUCTOR DRIVE

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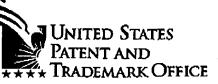
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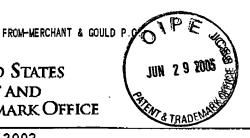
PATENT NUMBER:

FILING DATE: 05/03/2001

ISSUE DATE:

SHAREILL COLES, EXAMINER ASSIGNMENT DIVISION OFFICE OF PUBLIC RECORDS





50019.51 VS01/P04881

MARCH 19, 2002

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RECORDATION DATE: 11/08/2001

REEL/FRAME: 012492/0345

NUMBER OF PAGES: 3

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

OGLESBEE, JOHN W.

DOC DATE: 11/05/2001

ASSIGNEE:

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SERIAL NUMBER: 09848625

PATENT NUMBER:

FILING DATE: 05/03/2001

ISSUE DATE:

SAUNDRA BALLENGER, EXAMINER ASSIGNMENT DIVISION OFFICE OF PUBLIC RECORDS

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. :

US 6,768,355 B1

PAGE __1_ of __1_

DATED

July 27, 2004

INVENTOR(S):

Paul M. Henry et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page:

Beginning "(73) Assignee:

National Semiconductor Corporation,

Santa Clara, CA (US)"

should read -- (73) Assignee: National Semiconductor Corporation,

Santa Clara, CA (US); and

Motorola, Inc.

Schaumburg, IL (US)--

Column 6, Lines 63 and 64: "not inadvertently rest" should read --not inadvertently reset--

MAILING ADDRESS OF SENDER:

Merchant & Gould P.C. Attn: Brett A. Hertzberg P.O. Box 2903 Minneapolis, MN 55402-0903" PATENT NO.

US 6,775,112 B1

Docket No.

50019.51US01/P04881

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